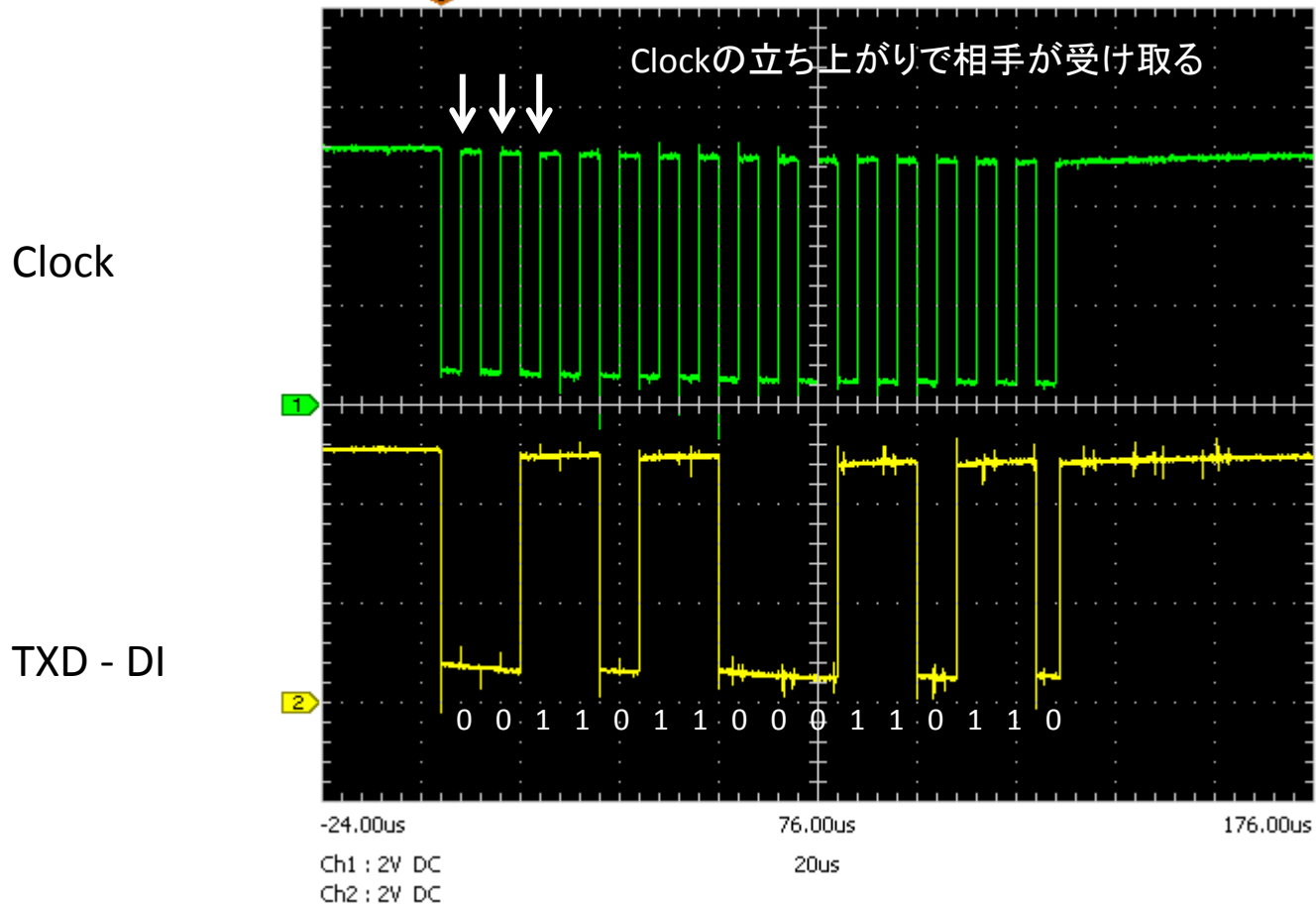


# main0 SLOW DMA SPI\_putData(str,2);

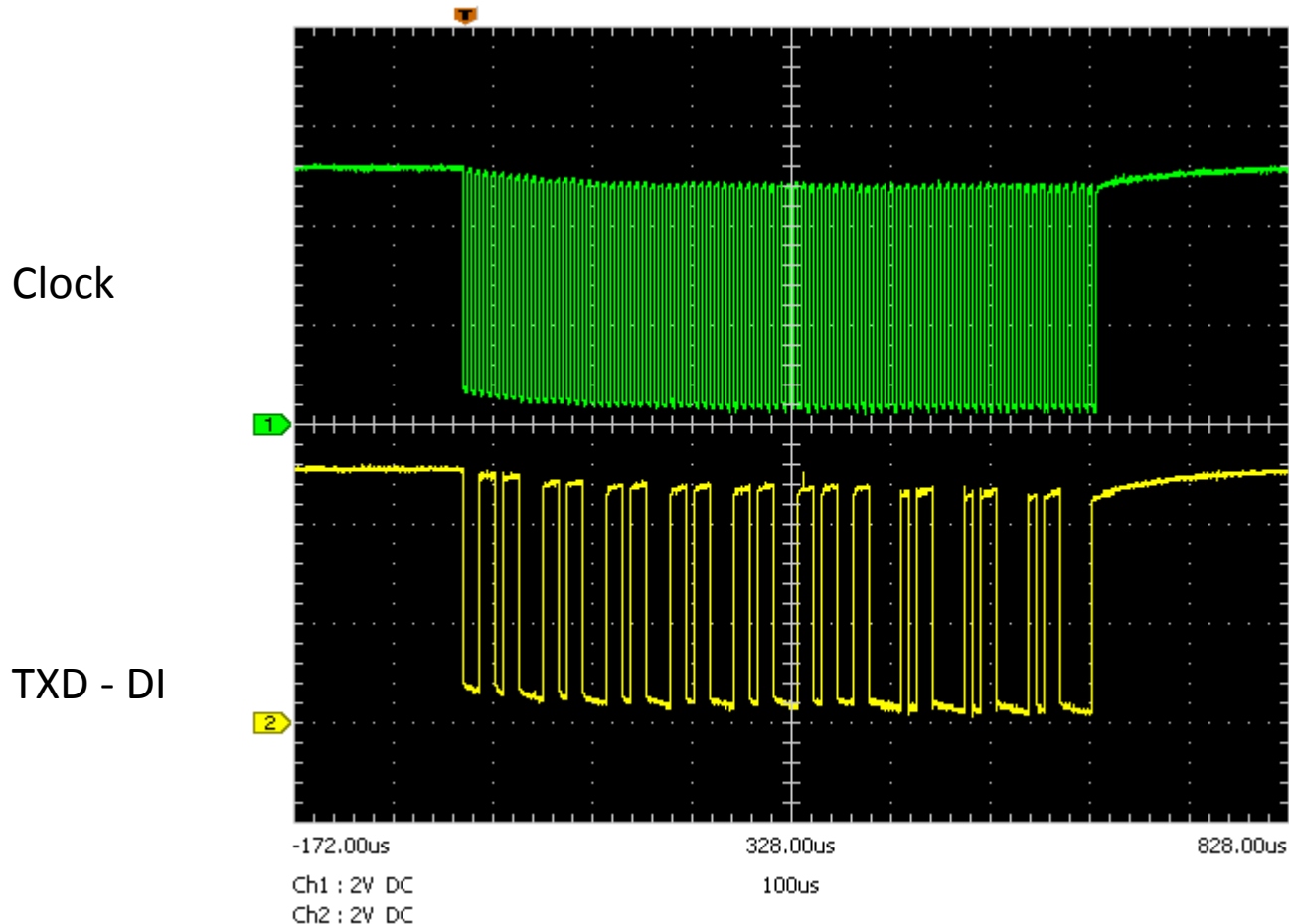
“66”を送信 (0x36[00110110]を2回送信)



test= 00 res=02

# main0 SLOW DMA SPI\_putData(str,10);

“666666aaaa”を送信 (0x36[00110110]を6回と0x61[01100001]を4回送信)



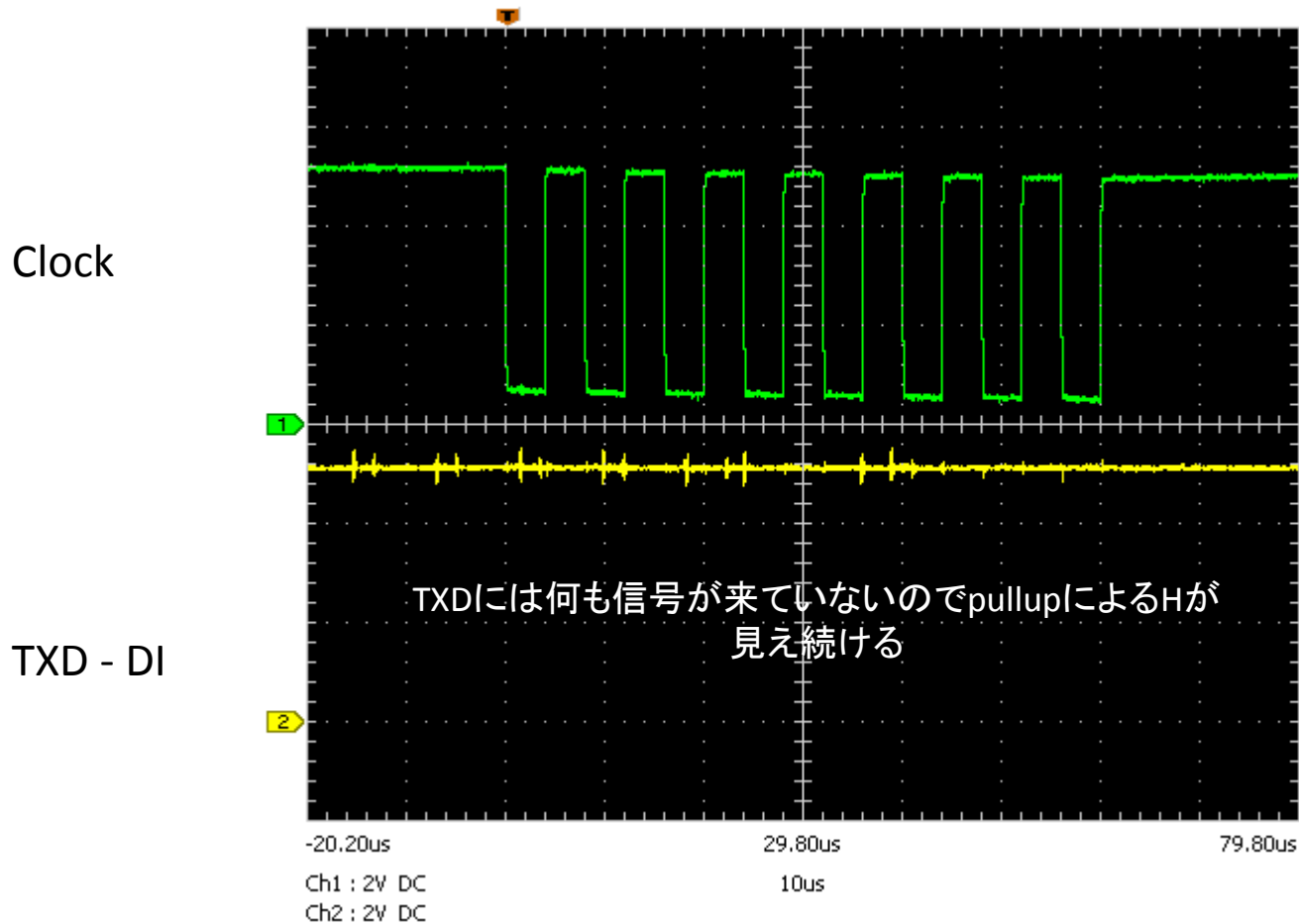
test= 01 res=0a

# main0 SLOW DMA

```
res=SPI_getData(str,1);
```

1byte受信なので、8回clockパルスが出る。立ち上がりで読み込む

1-Dec-2011 20:26:46



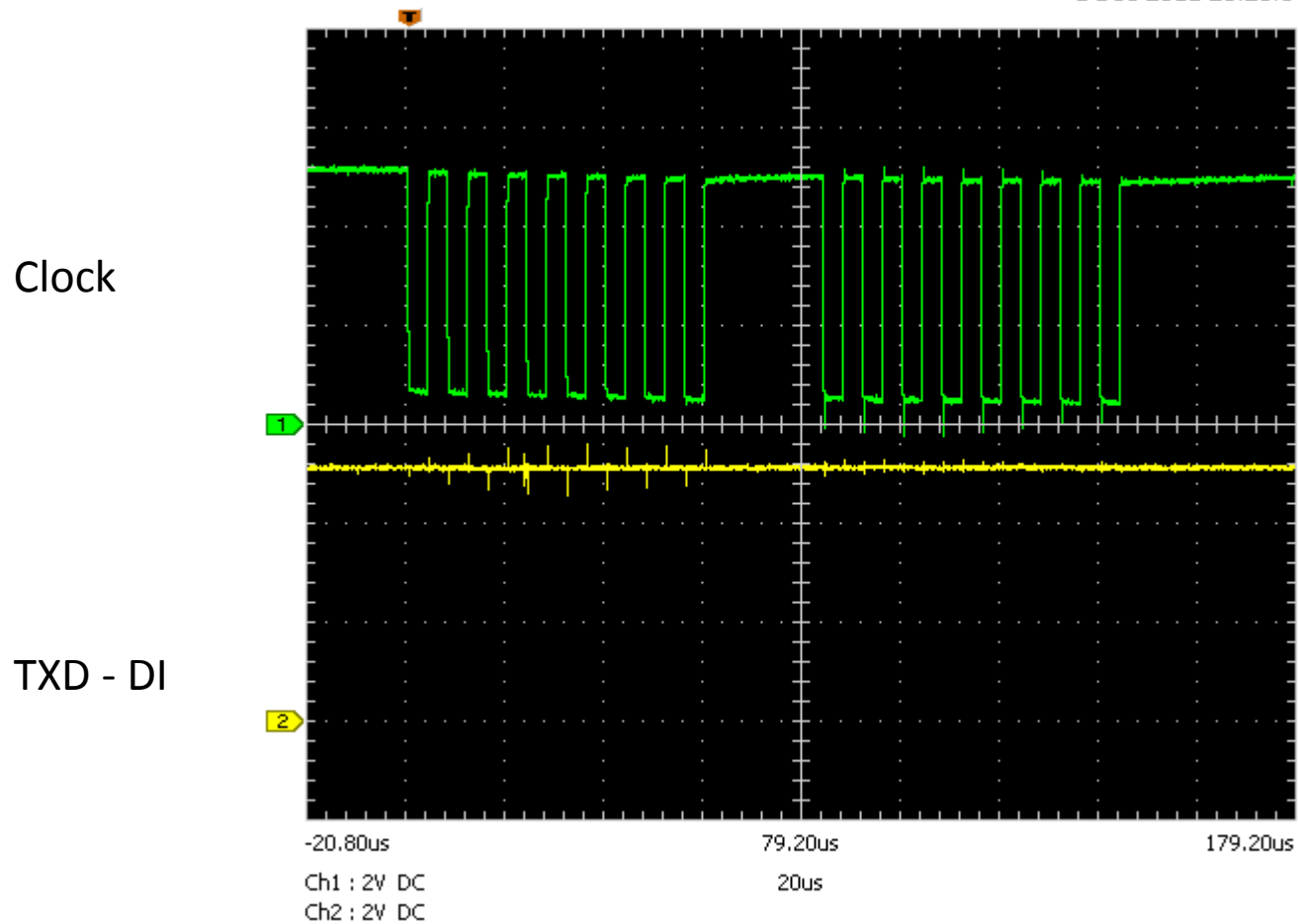
test= 02 res=01

# main0 SLOW DMA

```
res=wait_ready();
```

TXDがHに保たれていれば, 1byte受信が2回行われる

1-Dec-2011 20:28:6



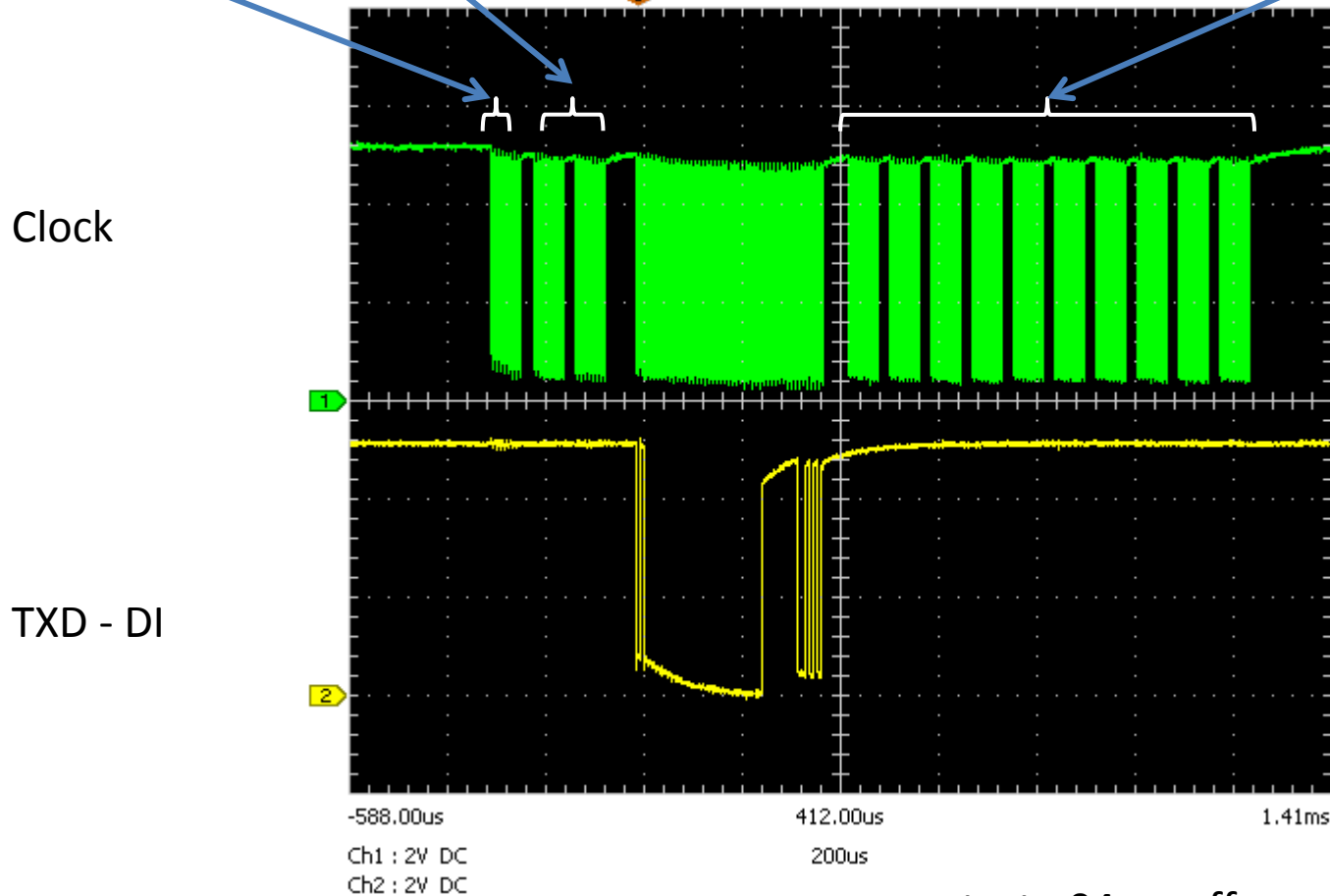
test= 03 res=00

# main0 SLOW DMA

```
res=send_cmd(0, 0xffL);
```

deselectで1byte, selectで2回byte読み出して, 0x40,0,0,0,0xff,0x95送信, そのあと10byte読み出し

1-Dec-2011 20:30:27



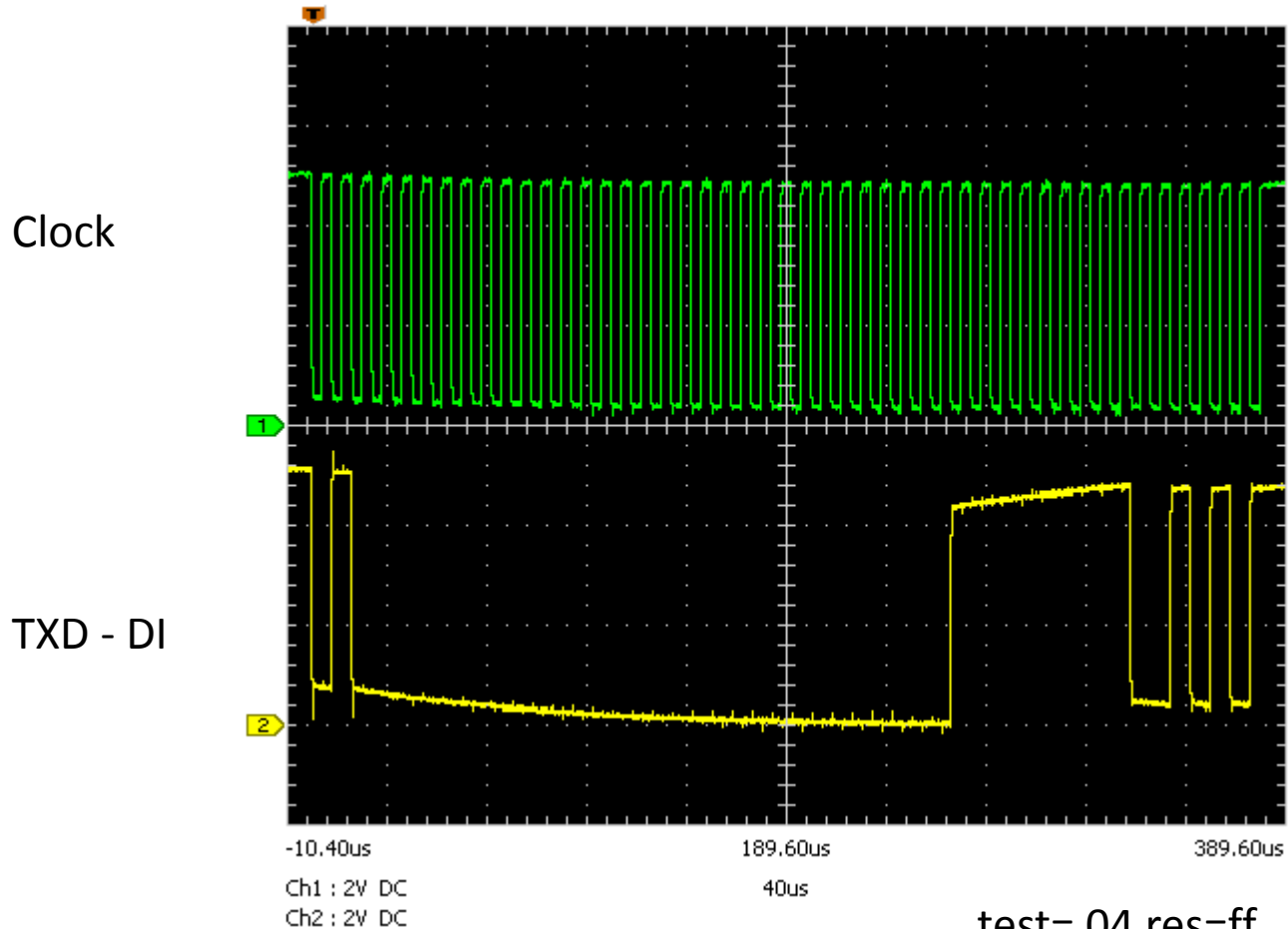
test= 04 res=ff

# main0 SLOW DMA

```
res=send_cmd(0, 0xffL);
```

0x40,0,0,0,0,0xff,0x95送信部分の拡大  
0x40:[01000000] 0xff:[11111111] 0x95:[10010101]

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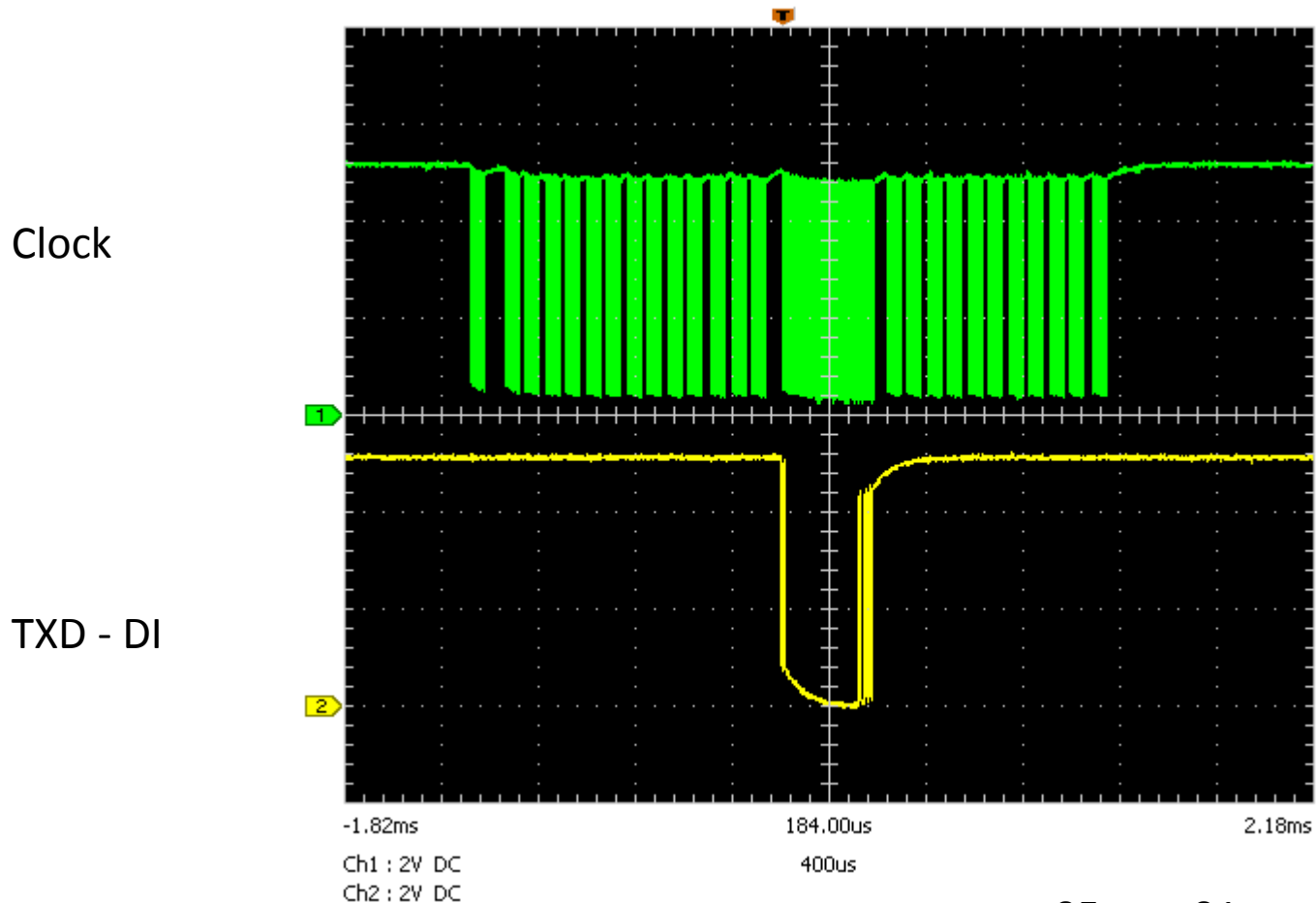


# main0 SLOW DMA

```
res=disk_initialize(DRIVE_NO);
```

deselectで1回読み出して、10回dummy読み出し、その後cmd0送信動作、10回dummy読み出し、最後にdeselectで1回読み出し

1-Dec-2011 20:33:12



test= 05 res=01